



Release Notes srn022

EP_NewUM Extension Pack Version 2 Beta Release

NOTE: Requires PSoC Designer Version 4.3

Release Date: October 6, 2006

Thank you for your interest in PSoC Designer™ version 4.3 extension pack for new User Modules beta. The information in this document lists installation requirements and describes software updates and changes.

System Requirements and Recommendations

<u>System Requirement</u>	<u>Minimum</u>	<u>Recommended</u>
▪ PSoC Designer Version	4.3 build 1884	4.3 build1884
▪ PSoC Programmer Version	2.20.0.13	2.20.0.13
▪ Processor Speed	500 MHz	1 GHz
▪ MB of RAM	256 MB	512 MB
▪ MB of Free Hard Drive Space	250 MB	300 MB
▪ Screen Resolution	1024x768	1280x1024
▪ CD-ROM Drive	✓	✓
▪ EPP Parallel Port		✓
▪ USB Port, preferably Open Host Controller or Universal	✓	✓
▪ Windows® 2000, XP (SP1)	✓	✓
▪ Microsoft Internet Explorer 6.0 (SP1)	✓	✓
▪ Adobe Reader (For Viewing of .PDF Documentation)	✓	✓

Updates

Check <http://www.cypress.com/psocdesigner>, http://www.cypress.com/ep_NewUM, and <http://www.cypress.com/pd43sp> for the latest downloads of software and documentation.

This Extension Pack Includes:

1. Digital Comm User Modules: SDCard.

This SD (Secure Digital) Card User Module is only supported on the part family CY8C29xxx. The SDCard User Module supports file systems FAT16 and above. Example projects are located in
...\Program Files\CypressMicroSystems\PSoCDesigner\Examples\SDCard.

2. Protocol User Modules: USBUART.

This USBUART Card User Module is only supported on the part family CY8C24x94.

3. Cap Sensors User Module: CSD.

This CSD User Module is only supported on the part family CY8C21x34.



User Module Errata

1. CSD: If you create sensors and sliders in the CSD Wizard and assign pins, and then set the number of sensors and sliders to 0, you may see odd overlaps in the display. If you click OK to close the Wizard and reopen it, the view will be restored to normal.
2. CSD: The diplex table shown in the CSD User Module data sheet does not match diplex tables created when the project is generated.
3. CSD: For a given slider resolution, the centroid is output as a value from 0 to the selected resolution, inclusive. For example: a resolution of 128 yields an output between 0 and 128, not 0 and 127.
4. CSD: There is no warning if all sensors are not assigned GPIO pins in the CSD Wizard.
5. CSD: The slider centroid could overflow when the resolution is large.
6. CSD: The group table listed in the Low Level API section only shows one byte for Slice Multiplier and indicates that the Diplex byte is not currently implemented. This is incorrect.

For assistance go to <http://www.cypress.com> or contact our Application Team at 425.787.4814.

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